

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/017,179 CHEUNG ET AL.	
		Examiner	Art Unit Aaron D Matthew	2114 Page 1 of 1

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	B	US-4,672,534	06-1987	Kamiya, Shigeo	714/30
	C	US-			
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	L	US-			
	M	US-			

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	U	United States Patent Application Publication□□Cheung et al.□□On-Chip Hardware Breakpoint Generator with Comprehensive Memory Operation Detection□□Pub. No.: US 2002/0188813 A1□□Appl. No.: 10/128,025□□Pub. Date: Dec. 12, 2002
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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